

VLSI lab

S.No	Specification	Qty
01.	<p><u>VLSI development Platform (Universal for FPGA & CPLD)</u></p> <p>Mother Board Specifications :</p> <p>On board : +5V, 3.3V, 2.5V,1.8V supply</p> <p>LCD display : 16 X 2 Character LCD</p> <p>7 Segment : 4 digits</p> <p>8 Channel ADC : 8-bit Resolution</p> <p>Push button : 4 ; Output LEDs : 16 ;DIP switches : 16</p> <p>HEX keypad : 1 ;DAC : 8-bit Resolution ;</p> <p>Memory : SRAM 32KB</p> <p>Peripheral Interface : Serial, PS2, VGA, USB etc.</p> <p>FPGA Daughter Card : Spartan 3, XC3S400PQ208</p> <p>Device density : 400k gates ; On board : 8 MHz crystal</p> <p>Master reset Key : For hardware reset</p> <p>On board : Flash EPROM for FPGA boot.</p> <p>Configuration Method : JTAG/Boundary scan interface ; PROM interface</p> <p>CPLD Daughter Card : CPLD, XC95108TQ100</p> <p>Device density : 2400 gates, 108 macro cells.</p> <p>On board : 8 MHz crystal</p> <p>Configuration Method : JTAG interface (boundary scan)</p> <p>Std. Accessories to be supplied with Kit :</p> <p>FPGA & CPLD JTAG Cable ,Male to Female serial cable.</p> <p>USB Cable ,Power Supply +12 V ,Xilinx Web pack CD External DC Power supply with outputs: 0-30V/1A Variable , 0 to ± 15V/1A Variable & 5V/1A Fixed with Digital display for Voltage & current . Outputs should be short circuit proof & over load protected.</p>	03