

JAMIA HAMDARD
Department of Computer Science & Engineering
School of Engineering Sciences & Technology
New Delhi-110062

Date Sheet of Supplementary/Improvement Examination for Odd Semester-2020

July 9, 2021

Note: Timing for Theory paper: 10:00 AM to 1:00 PM and Lab paper: 1:30 PM to 5:30 PM

Date/Day	Theory Timing: 10:00 AM- 1:00 PM	Lab Timing: 01:30 PM- 5:30 PM
July 13, 2021 Tuesday	BTCSE 101: Applied Physics – I BTCSEAI 101: Applied Physics – I BTCSE 103: Applied Chemistry (For 2017 Batch)	BTCSE 105: Applied Physics-I Lab
July 14, 2021 Wednesday	BTCSE 302: Discrete Mathematics (For 2017 Batch) BTCSE DE35: Distributed Computing (DE-3) BCA 302: Introduction to Database Management System	BTCSEAI 106: Basic Electrical Engineering Lab
July 15, 2021 Thursday	BTCSE 104: Engineering Graphics & Design BTCSEAI 104: Engineering Graphics & Design BTECE 104: Engineering Graphics & Design BTCSE 104: Engineering Mechanics (For 2017 Batch)	BTCSE 107: Engineering Graphics & Design Lab BTCSEAI 107: Engineering Graphics & Design Lab BTECE 107: Engineering Graphics & Design Lab
July 16, 2021 Friday	BTCSE 303: Analog and Digital Electronics (For 2017 Batch)	

V. Arshad

Farheen

July 17, 2021 Saturday	BTCSE 304: Data Structures (For 2017 Batch)	
July 19, 2021 Monday	BTCSE 305: Computer Organization and Architecture (For 2017 Batch)	

M. Afshar Afam.

Dean, SEST

Prof. M. Afshar Afam
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Copy to: Controller of Examinations & Admissions

Farheen
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