


### Profile of the Faculty Member

1.	Name of the Faculty member	<b>Dr. BHAVYA ALANKAR</b>	
2.	Designation	<b>Assistant Professor (Stage III)</b>	
3.	Qualifications	Ph.D. (VLSI Design), M.Tech. (VLSI Design) CDAC, Mohali B.Tech. (CSE)	
4.	State of Domicile	New Delhi	
5.	Department & School	Computer Science and Engineering, SEST	
6.	Details of Courses taught	VLSI Design, VHDL, Software Engineering,	
7.	Research Thrust Areas	Reconfigurable Computing, VLSI Design, ICT for Development, Smart Cities and IoT	
8.	Emails	balankar@jamiahamdard.ac.in bhavya.alankar@gmail.com	
<b>PUBLICATIONS</b>			
9.	Publications in peer reviewed National & International journals, Citations, H-index	For Publications Profile view at: <b>Google Scholar:</b> <a href="https://scholar.google.co.in/citations?user=fcEm8tAAAAAJ&amp;hl=en">https://scholar.google.co.in/citations?user=fcEm8tAAAAAJ&amp;hl=en</a> <b>Scopus Author ID:</b> ORCID: 0000-0003-1329-0249 Give LINKS <a href="https://www.researchgate.net/profile/Bhavya-Alankar">https://www.researchgate.net/profile/Bhavya-Alankar</a>	
<b>BOOKS &amp; BOOK CHAPTERS PUBLISHED</b>			
10.	Number of books published by National publisher	01 (Techguru Publishers)	
11.	Number of books published by international publisher	01 (02 In Press) (IGI Global, CRC Press (US), Springer (US))	

12.	Number of Book Chapters in Edited books of National Publishers	8 (IGI Global, CRC Press (US), Springer (US))
13.	Number of Book Chapters in Edited books of International Publishers	20 (Springer (US), IGI Global, CRC Press (US) , Routledge (UK))
<b>RESEARCH GRANTS AND CONSULTANCY PROJECTS</b>		
14.	Number of research grants from govt. funding agencies as PI/Coordinator	Pending
16.	Number of research grants from International and private sources as PI	Applied for
<b>RESEARCH SUPERVISION</b>		
<b>As Supervisor</b>		
17.	Number of Ph.D. Guided	In Process
18.	Number of Master's theses/dissertations Guided	100
<b>As Co-supervisor</b>		
19.	Number of Ph.D. Guided	08
20.	Number of Master's theses/dissertations Guided	20
<b>PATENTS</b>		
21.	Number of Patents Granted	02 (01 (Govt. of India) (National patent) 01 (International Patent))
22.	Number of Patents Applied for	01 (01 National patent) : Pending
<b>PRESENTATIONS IN CONFERENCES AS SPEAKER/ RESOURCE PERSON</b>		
23.	Number of presentations in National or International Conferences in India	10 (Invited Speaker)

24.	Number of presentations in International Conferences abroad	35
<b>NATIONAL AWARDS, HONOURS AND FELLOWSHIPS RECEIVED</b>		
25.	<ol style="list-style-type: none"> <li>1. Fellow, Institution of Electronics and Telecommunication Engineers (IETE)</li> <li>2. Life Time Member, Indian Society for Technical Education (ISTE)</li> <li>3. Life Time Member, Computer Society of India (CSI)</li> <li>4. Life Time Member, Indian National Science Academy (INSA)</li> </ol>	
<b>INTERNATIONAL AWARDS, HONOURS AND FELLOWSHIPS RECEIVED</b>		
26.	<ol style="list-style-type: none"> <li>1. ACM Professional Member</li> <li>2. Life Time Member –IEEE</li> </ol>	
<b>STAFF DEVELOPMENT/REFRESHER/SHORT TERM TRAINING PROGRAMME/WEBINARS ORGANIZED</b>		
27.	<ol style="list-style-type: none"> <li>1. Organizing Committee Member, 3rd International Conference on ICT for Digital, Smart and Sustainable Development, Department of CSE, SEST, Jamia Hamdard, March 24-25, 2022</li> <li>2. Organizing Committee Member, 2nd International Conference on ICT for Digital, Smart, and Sustainable Development, ICIDSSD 2020, 27-28 February 2020, Jamia Hamdard, New Delhi, India</li> <li>3. Organizing Committee Member, 3<sup>rd</sup> International Webinar on “COVID 19: Opportunities and Challenges for Digital Environments” on 6<sup>th</sup> July 2020</li> <li>4. Organizing Committee Member, International Conference on “ICT for Digital, Smart, and Sustainable Development” (ICIDSSD '18), Jamia Hamdard, New Delhi, India</li> <li>5. Organized SERB-DST (Govt. of India) Sponsored National Workshop on Challenges in Data Analytics and its Applications, 16<sup>th</sup> Oct. 2017</li> </ol>	
<b>MEMBER(S) OF NATIONAL COMMITTEES</b>		
28.	<ol style="list-style-type: none"> <li>1. NCC Cadet Officer, 2014 – 2019</li> </ol>	

<b>MEMBER(S) OF INTERNATIONAL COMMITTEES</b>	
<b>29.</b>	<ol style="list-style-type: none"> <li>1. Member, IAENG, Singapore</li> <li>2. Reviewer, Elsevier Journals</li> <li>3. Reviewer, Editorial Board, Springer Journals</li> </ol>
<b>ADMINISTRATIVE RESPONSIBILITIES/ PARTICIPATION IN COMMITTEES OF JAMIA HAMDARD</b>	
<b>30.</b>	<ol style="list-style-type: none"> <li>2. Honorary Incharge, Placement Coordinator 2011- till date</li> <li>3. Member, Board of Studies 2022-till date</li> <li>4. NCC Officer, 2014 – 2019</li> <li>5. Coordinator Admission Committee, B.Tech. (CSE), 2018 – till date</li> <li>6. Member, SRAC Committee, 2011-2016</li> <li>7. Course Coordinator M.Tech. (CS) 2018</li> <li>8. Course Coordinator, M.C.A. 2018</li> <li>9. Coordinator, Fee Monitoring Committee, 2019</li> <li>10. Coordinator, CSI Student Chapter 2019</li> <li>11. Member, Program Committee, B.Tech. Induction Program 2019</li> </ol>